United States Patent [19]

DiSanto et al.

[11] **Patent Number: 4,598,960**

[45] **Date of Patent: Jul. 8, 1986**

[54] **METHODS AND APPARATUS FOR CONNECTING CLOSELY SPACED LARGE CONDUCTOR ARRAYS EMPLOYING MULTI-CONDUCTOR CARRIER BOARDS**

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[21] Appl. No.: **728,602**

[22] Filed: **Apr. 29, 1985**

[51] **Int. Cl.**[4] **H01R 9/09**

[52] **U.S. Cl.** ..... **339/17 M**; 361/412; 29/830

[58] **Field of Search** ..... 339/17 M, 17 LM, 17 CF, 339/17 N; 361/393-395, 398, 399, 412, 413, 414; 315/169.1-169.4; 29/829-834

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[57] **ABSTRACT**

Apparatus is disclosed for connecting to a plurality of closely spaced lines located on a display panel. The panel is positioned in a base plate assembly which serves as a frame for the panel. The base assembly has a plurality of first and second locating pins arranged along a side of the panel with a set of pins positioned with respect to an aperture on said base assembly. A plurality of carrier boards are employed to connect to the closely spaced lines, wherein each board is positioned between associated pins and overlies the aperture. When in this position, contact areas on the underside of the board are forced to contact signal lines which underlie the aperture by means of a conductive coupling strip positioned in each aperture. The top of the carrier board contains output conductors which are connected to the display lines by means of another conductive strip located on the top of the carrier boards and directed through a channel.

20 Claims, 7 Drawing Figures